Notice of References Cited Application/Control No. 10/544,894 Examiner TUAN A. VU Applicant(s)/Patent Under Reexamination DASU ET AL. Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	Α	US-7,155,708	12-2006	Hammes et al.	717/155
*	В	US-2001/0016936	08-2001	Okada et al.	716/18
*	C	US-6,745,160	06-2004	Ashar et al.	703/14
*	D	US-5,491,823	02-1996	Ruttenberg, John C.	717/161
*	Е	US-5,572,436	11-1996	Dangelo et al.	716/18
*	F	US-6,493,863	12-2002	Hamada et al.	716/18
*	G	US-5,764,951	06-1998	Ly et al.	716/1
*	Н	US-6,507,947	01-2003	Schreiber et al.	717/160
*	ı	US-5,671,432	09-1997	Bertolet et al.	712/11
*	J	US-5,943,499	08-1999	Gillies et al.	717/155
*	K	US-5,502,645	03-1996	Guerra et al.	716/18
*	L	US-7,120,879	10-2006	Gutberlet et al.	715/853
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					
	Р					
	Ø					
	R					
	Ø					
	Т					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)				
	U	Brisk et al, "Instruction Generation and Regularity Extraction for Reconfigurable Processors", CASES 2002, Oct 2002, pp. 1-8				
	V	Janssen et al, "A Specification Invariant Technique for Regularity Improvement between Flow-Graph Clusters", IEEE 1996, pg. 138-143				
	w	Cheng et al, "High-Level synthesis: Current Status and Future Prospects", June 1993 pp. 351-400				
	х					

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.